

Notice of References Cited	Application/Control No. 09/755,389		Applicant(s)/Patent Under Reexamination BANERJIA ET AL.	
	Examiner Andre R. Fowlkes		Art Unit 2192	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,815,720	09-1998	Buzbee, William B.	717/158
	B	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chilimbi, et al., Cache-Conscious Structure Definition, May 1999, Proceedings of the ACM SigPlan'99 Conference on PLD and I, pp. 1-12
	V	Jacob, Hardware/Software Architectures for Real-Time Caching, October 1999, CASES'99: Workshop on Compiler and Architecture Support for Embedded Systems, pp. 1-4
	W	Cohn, et al, Hot Cold Optimization of Large Windows/NT Applications, 1996, IEEE, pp. 80-89
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.